

Figure - 1

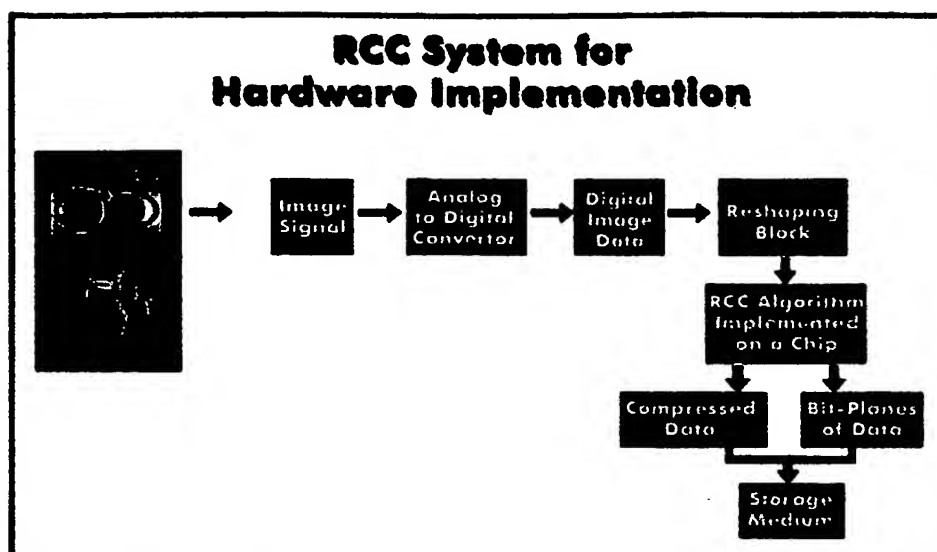
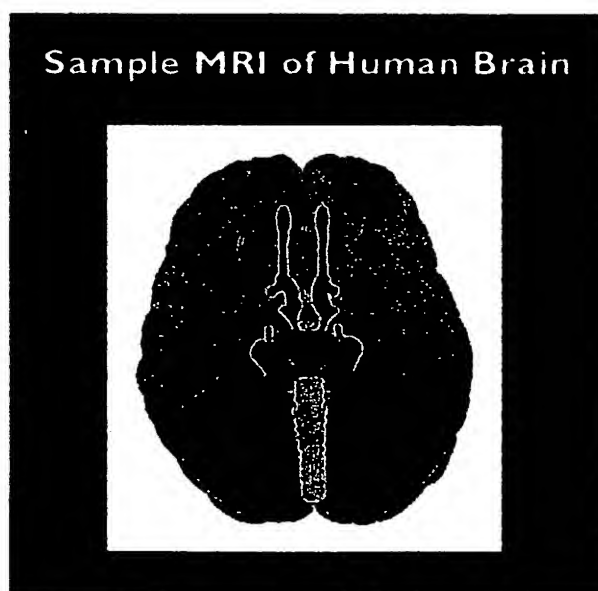
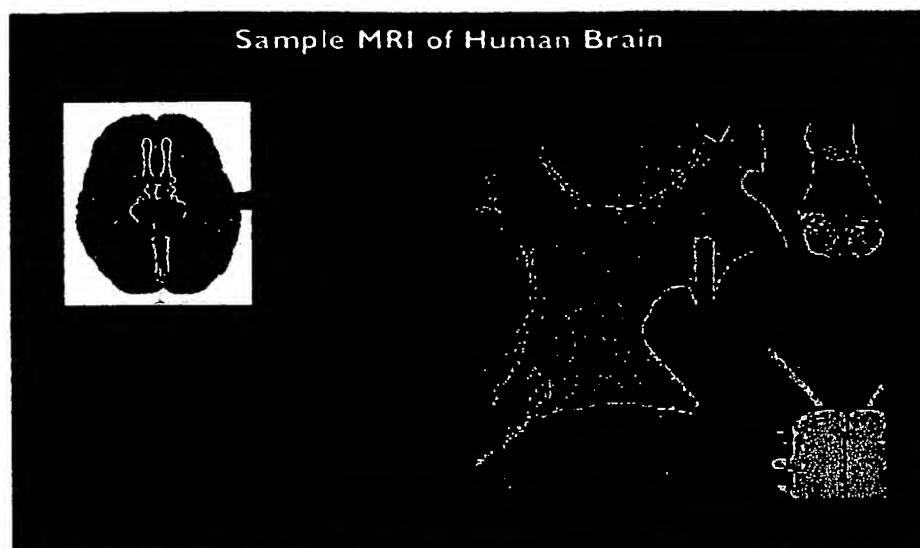


Figure - 2

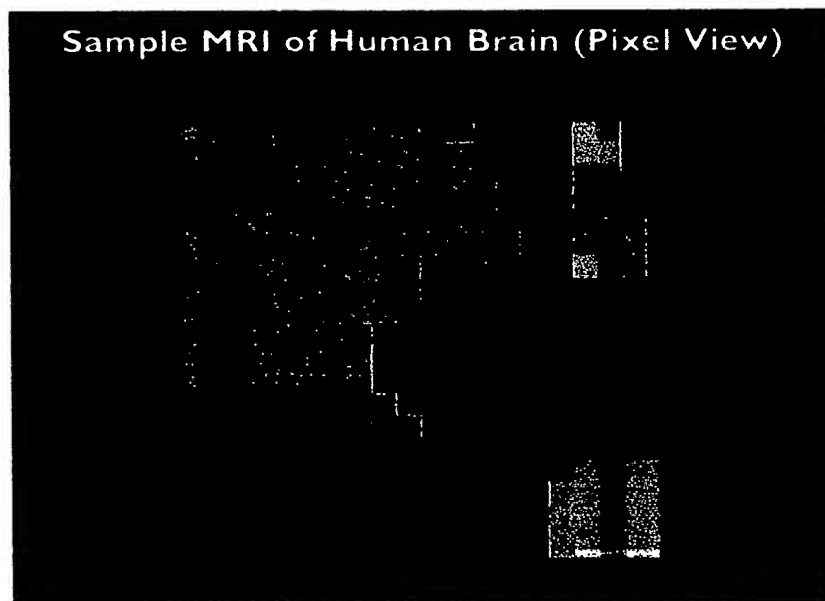


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**Figure - 3**



**Figure - 4**



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Figure - 5

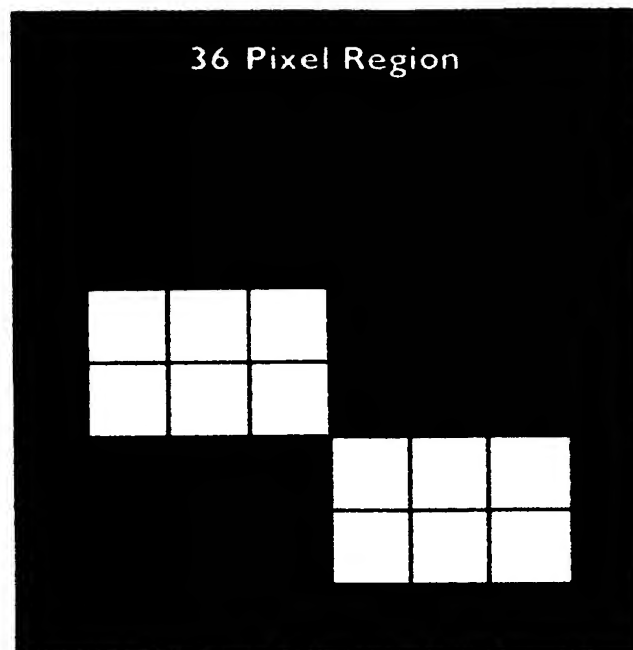
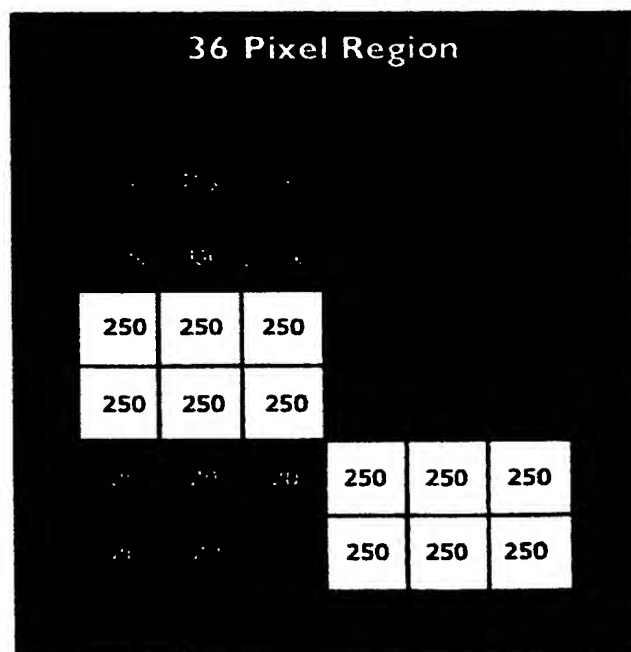


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Figure - 7

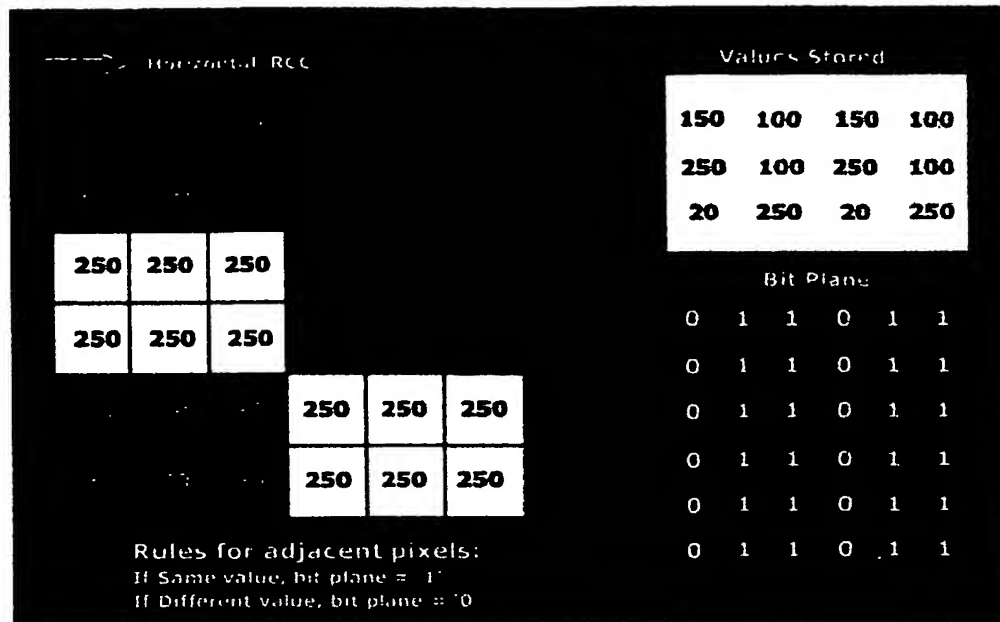


Figure - 8



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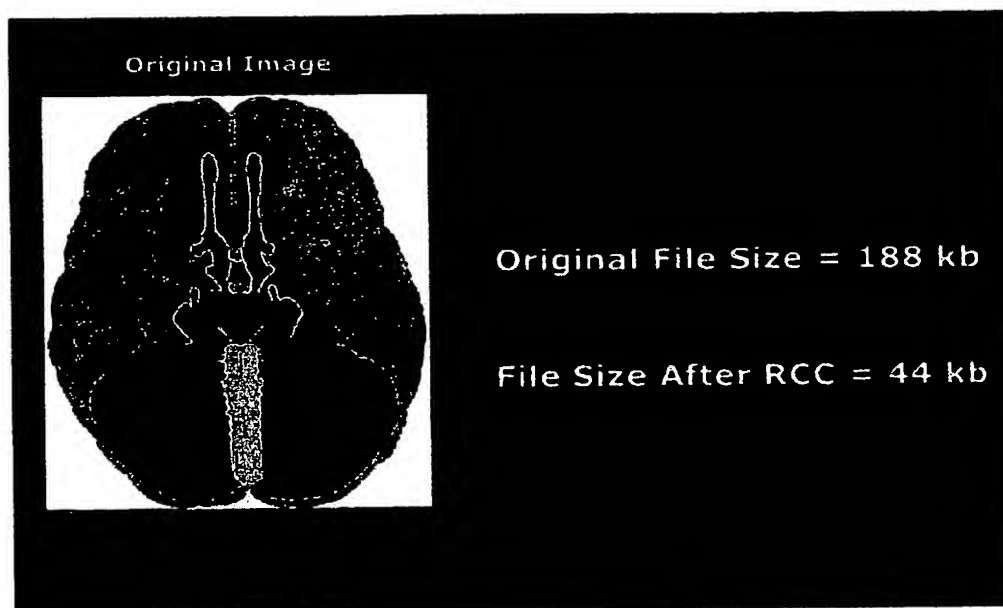
Figure - 9

Bit Plane						Bit Plane					
	1	1		1	1		0	0		0	0
0	1	1	0	1	1	1	1	1	1	1	1
	1	1	0	1	1		0	0	1	1	1
0	1	1	0	1	1	1	1	1	1	1	1
	1	1		1	1		0	0		0	0
0	1	1	0	1	1	1	1	1	1	1	1
Final Values Stored											
	150	100		250							
		20		250							

Figure - 10

Original Values						Final Values Stored					
	250	250		250			150	100		250	
	250	250		250				20		250	
				250	250	250					
				250	250	250					
Total Memory Required = 288 bits						After RCC Total Memory Required = 112 bits					

Figure - 11



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Figure - 12

